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** CONTINUING DATA *****

This application is a 371 of PCT/JP00/05044 07/28/2000

TD

Yes

** FOREIGN APPLICATIONS *****

JAPAN 11/228852 08/12/1999

JAPAN 11/230875 08/17/1999

JAPAN 11/266932 09/21/1999

JAPAN 11/269320 09/22/1999

JAPAN 2000-102769 04/04/2000

TD

Yes

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	Allowance Initials	JAPAN	36	33	9

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TITLE

Multilayer printed wiring board, solder resist composition, method for manufacturing multilayer printed wiring board, and semiconductor device

FILING FEE
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